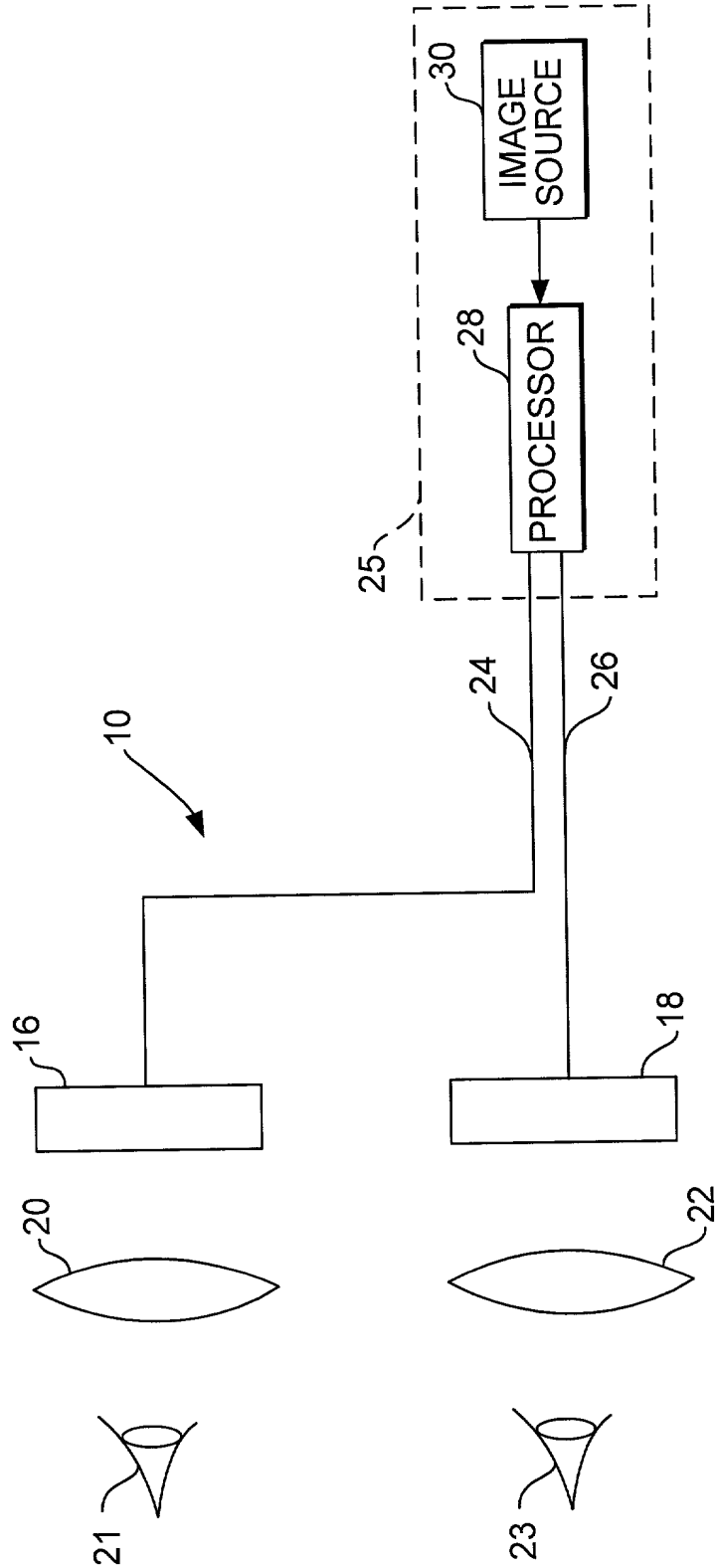


**FIG. 1**  
RELATED ART

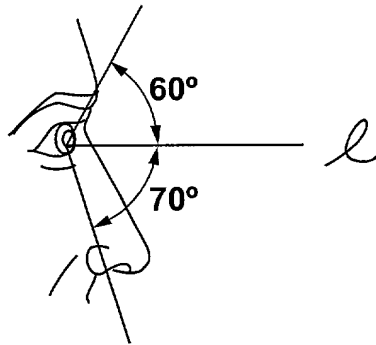


**FIG. 2**  
RELATED ART

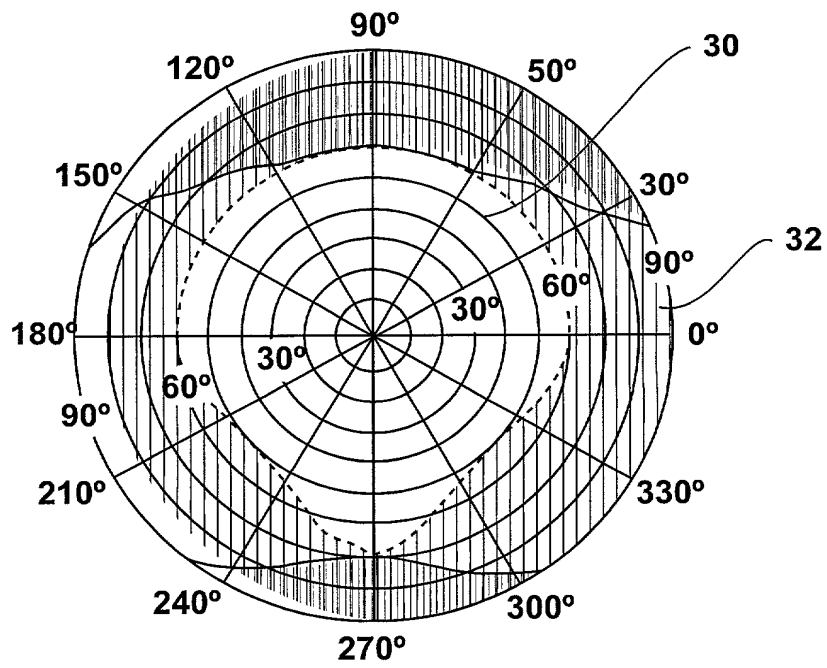
	1	....	....	n
1				
.				
.				
.				
.				
.				
.				
.				
n				

**FIG. 3**  
RELATED ART

FIG. 3 is a schematic diagram of a grid structure. The grid is composed of a series of rows and columns. The columns are labeled 1, ..., n at the top. The rows are labeled 1, ..., n on the left side. The grid is used to illustrate a method for determining the relationship between different elements in a system.



**FIG. 4**



**FIG. 5**

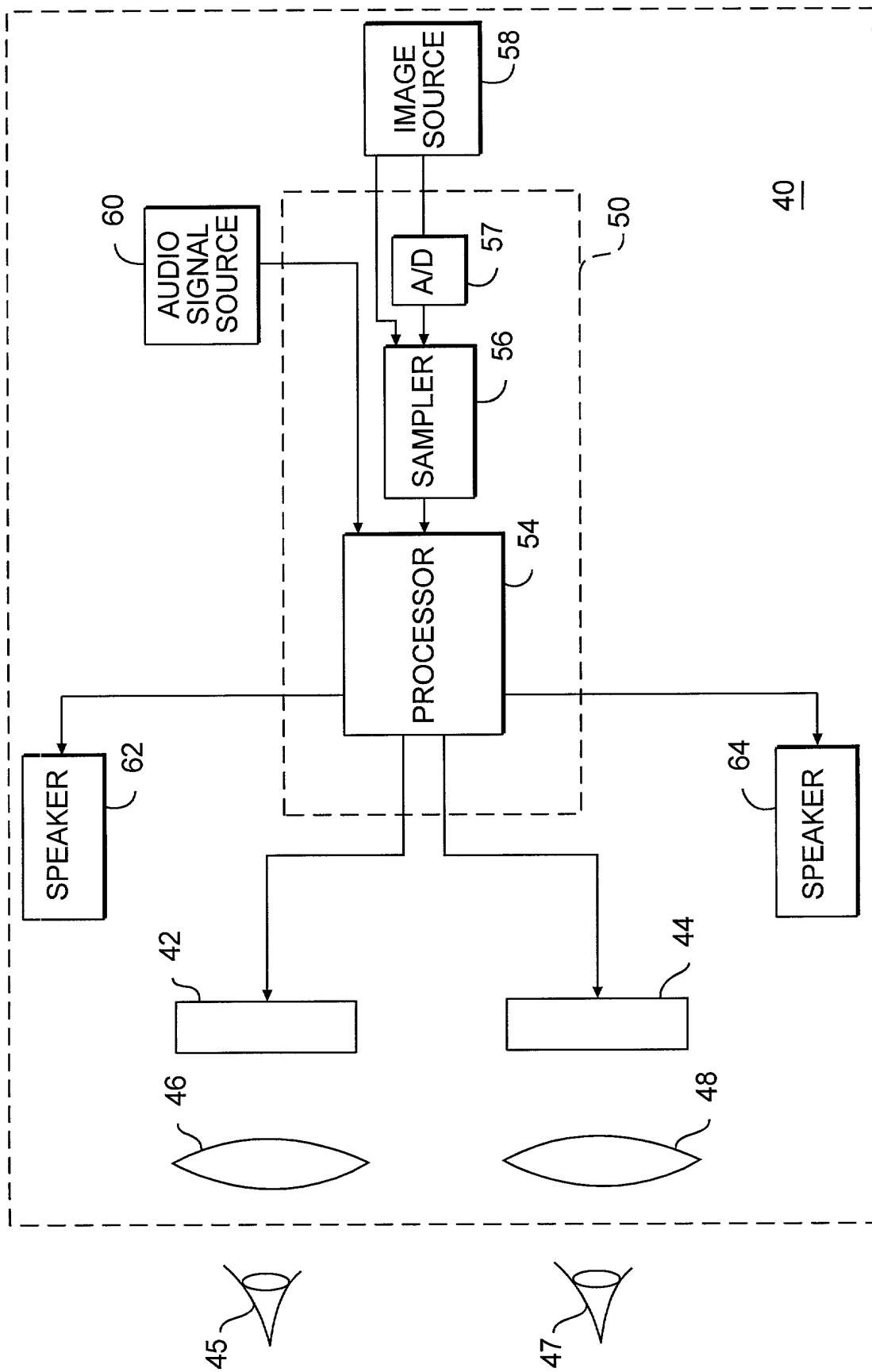
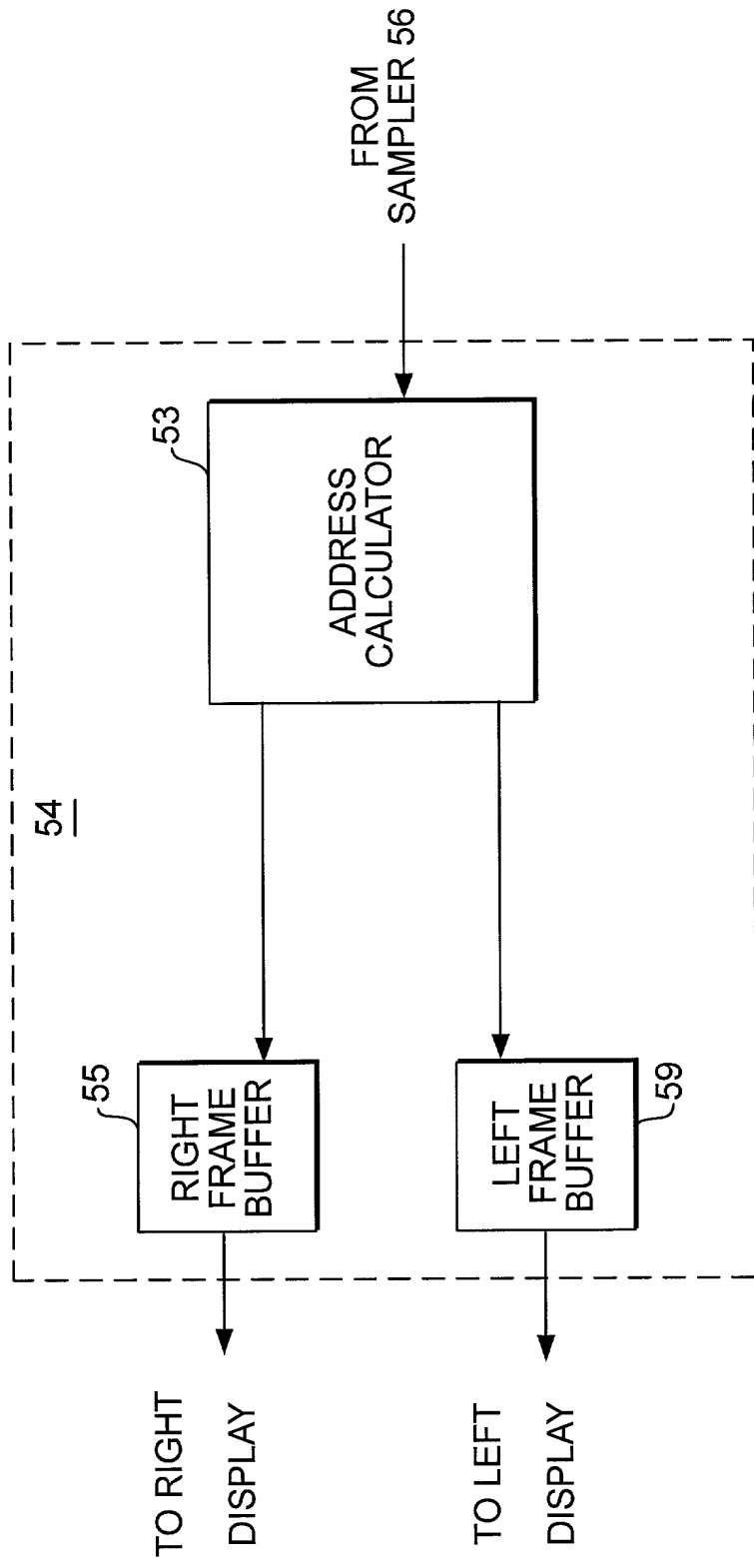
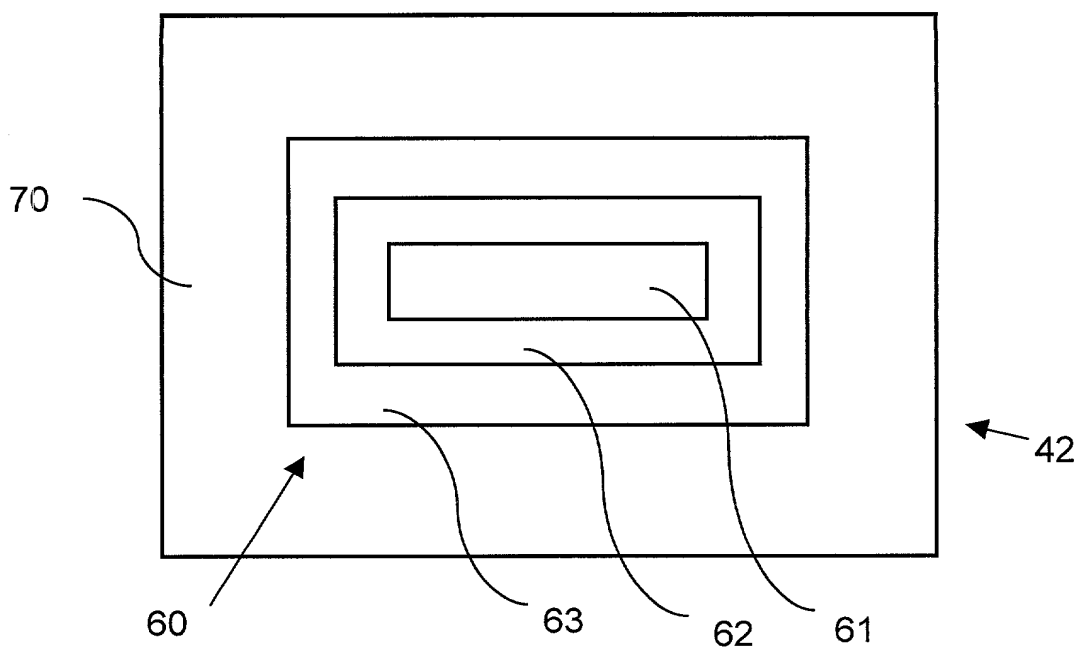


FIG. 6

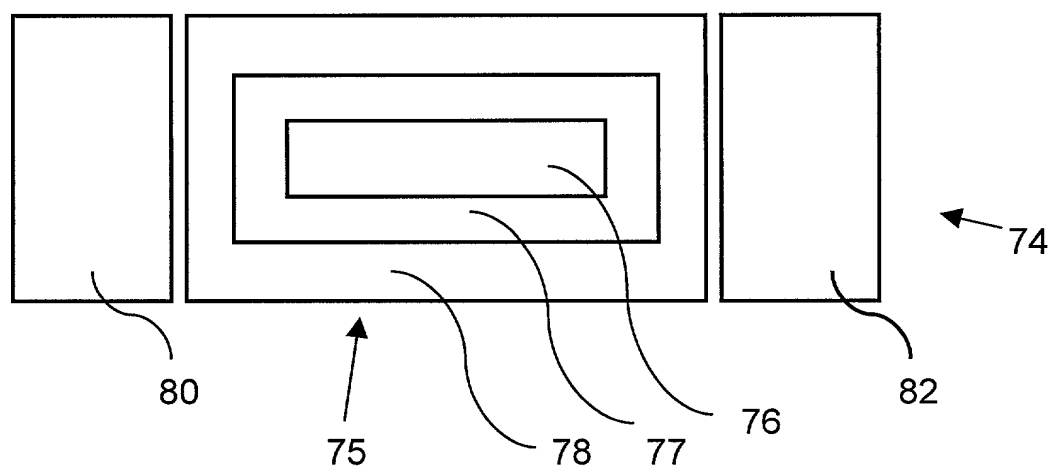
FIG. 7 is a block diagram of a system for processing video data. The system includes a video source 10, a video processor 20, and a video display 30. The video source 10 provides video data to the video processor 20, which processes the data and outputs it to the video display 30. The video processor 20 includes a video buffer 22, a video decoder 24, and a video encoder 26. The video buffer 22 stores video data received from the video source 10. The video decoder 24 decodes the video data, and the video encoder 26 encodes the decoded video data. The video display 30 displays the video data received from the video processor 20.



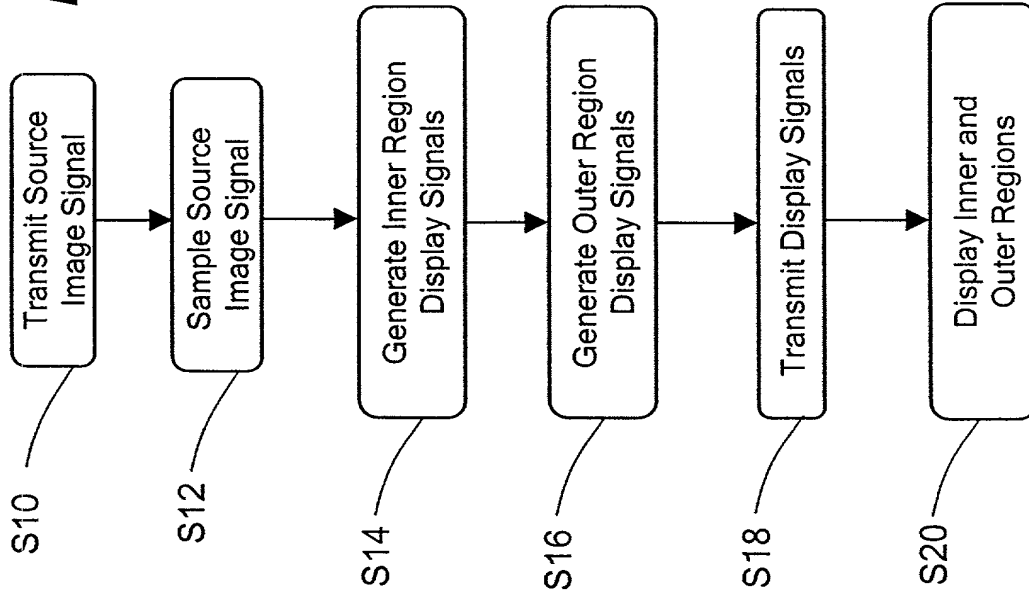
**FIG. 7**



**FIG. 8**

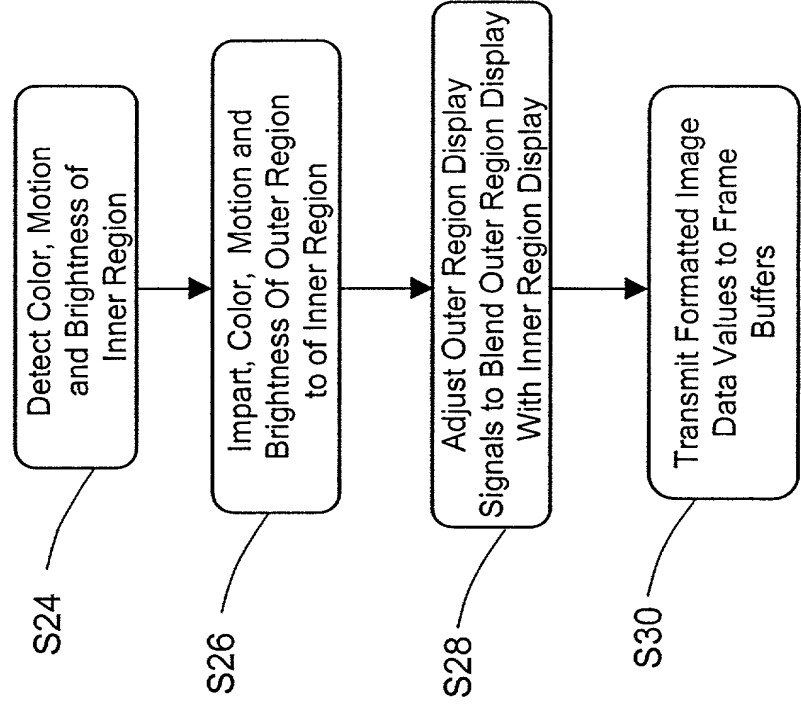


**FIG. 11**



**FIG. 9**

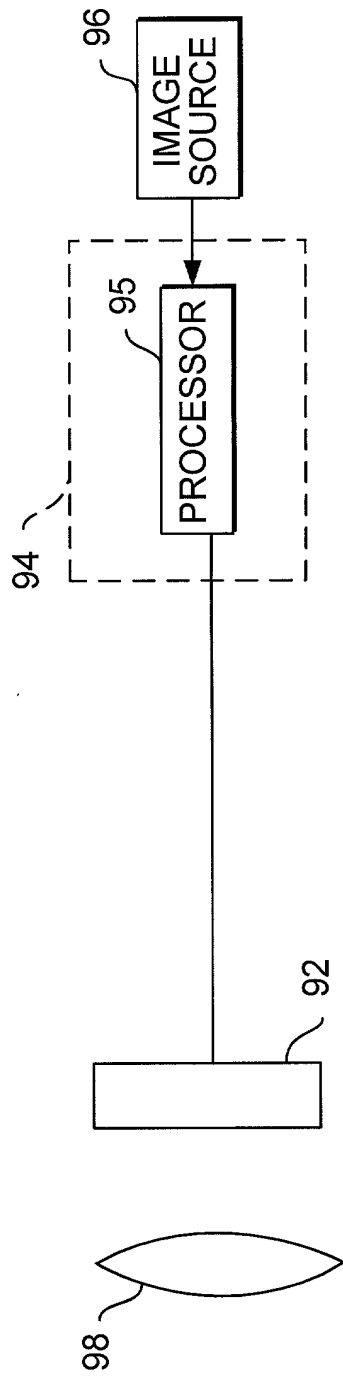
S16



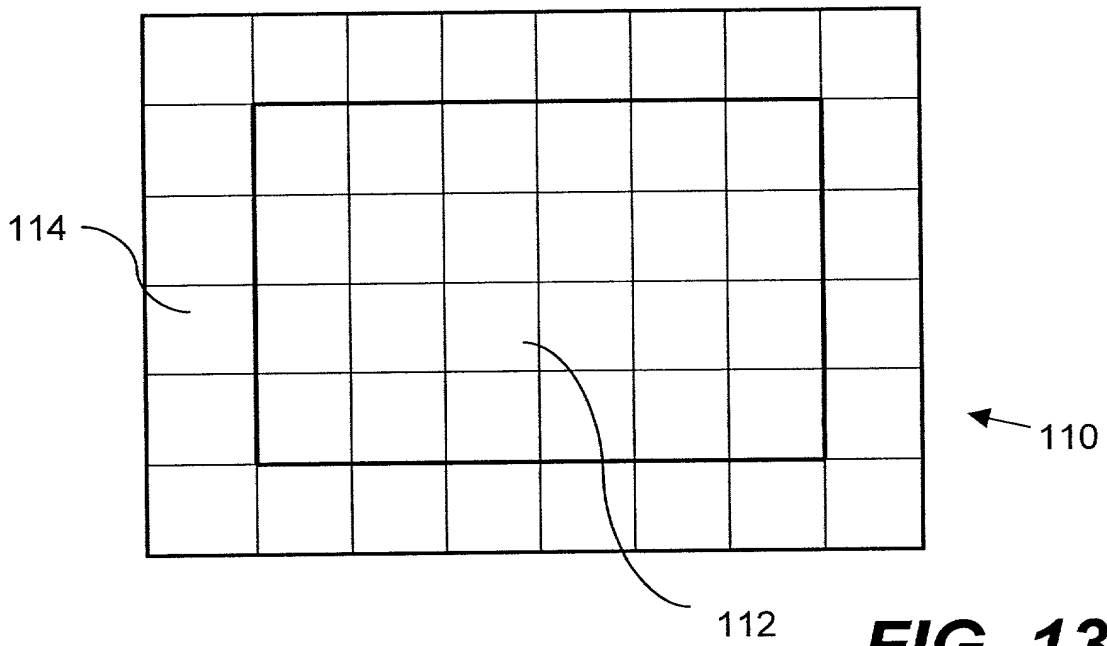
**FIG. 10**



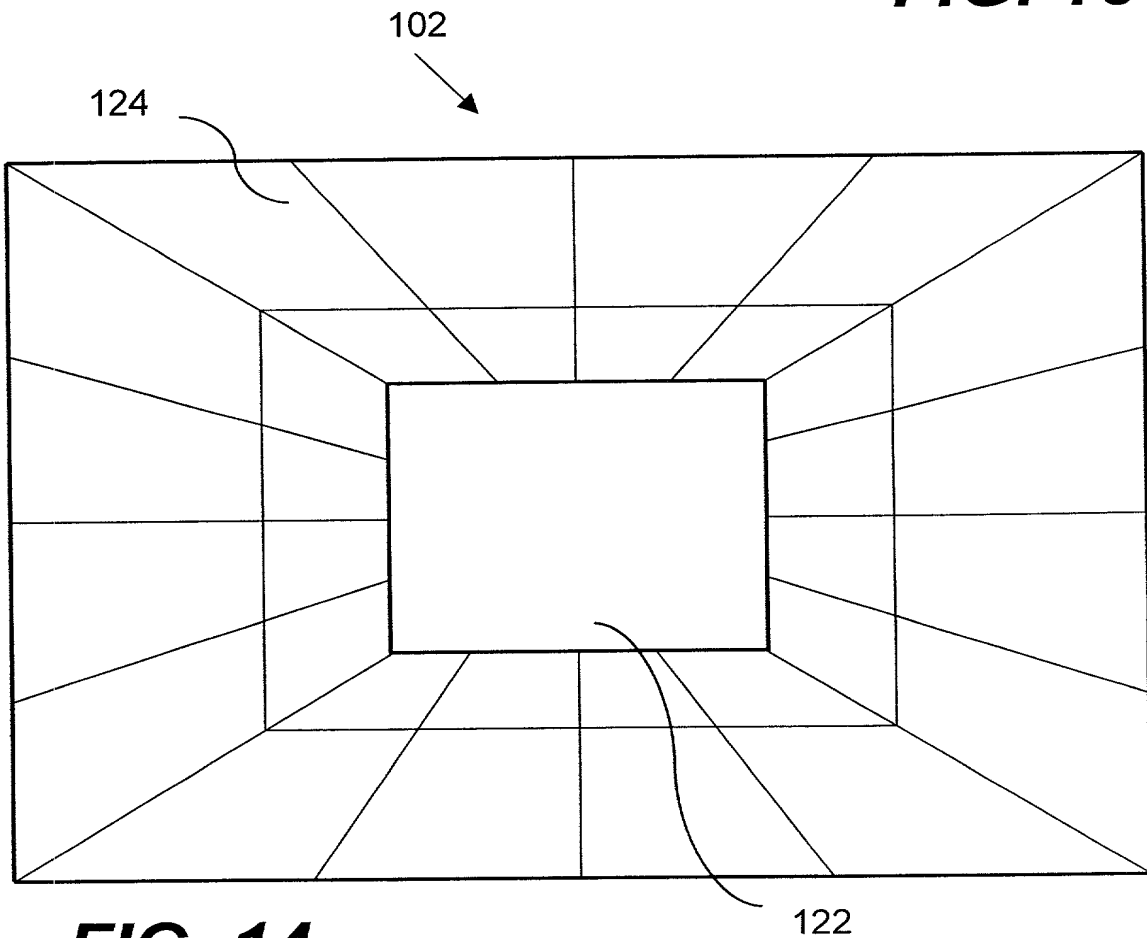
90



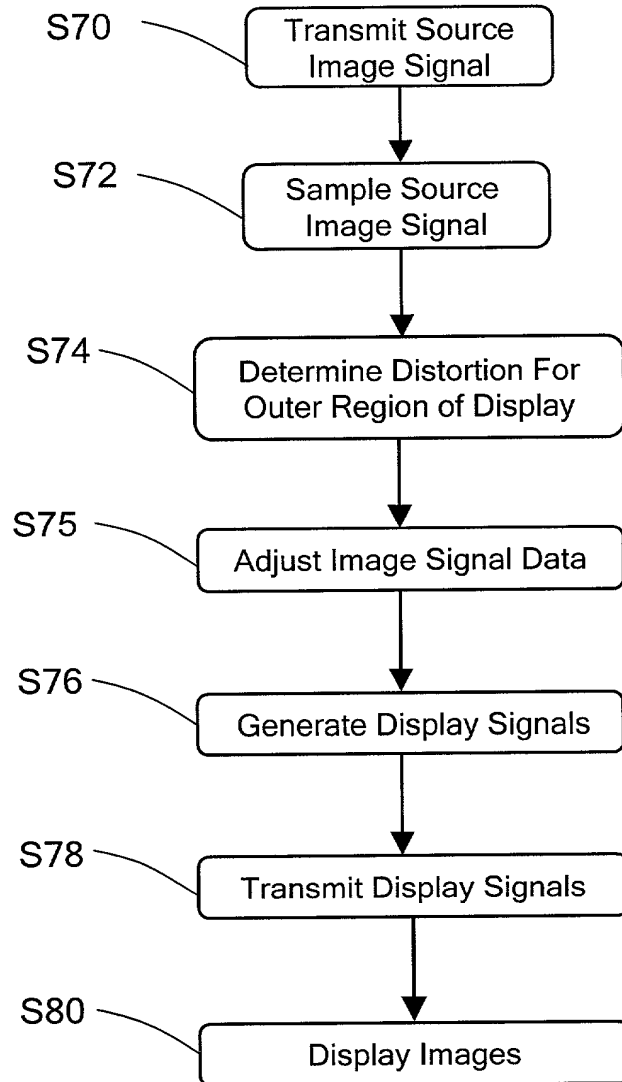
**FIG. 12**



**FIG. 13**

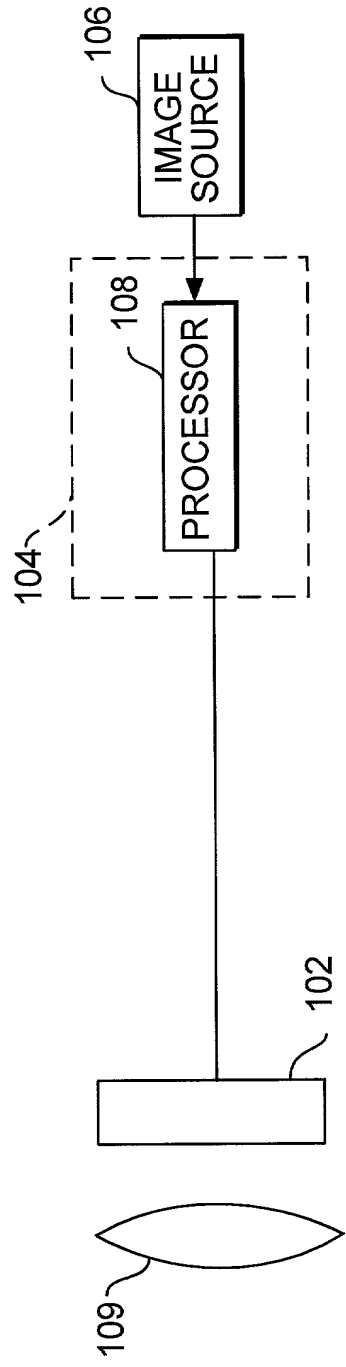


**FIG. 14**



**FIG. 15**

100



**FIG. 16**